Preferential Syntheses of Semiconducting Vertically Aligned Single-Walled Carbon Nanotubes for Direct Use in FETs

Liangti Qu, Feng Du, and Liming Dai*

Department of Chemical and Materials Engineering, School of Engineering, University of Dayton, 300 College Park, Dayton, Ohio 45469

Received April 4, 2008; Revised Manuscript Received June 8, 2008

ABSTRACT

We have combined fast heating with plasma enhanced chemical vapor deposition (PECVD) for preferential growth of semiconducting vertically aligned single-walled carbon nanotubes (VA-SWNTs). Raman spectroscopic estimation indicated a high yield of up to 96% semiconducting SWNTs in the VA-SWNT array. The as-synthesized semiconducting SWNTs can be used directly for fabricating FET devices without the need for any postsynthesis purification or separation.

Carbon nanotubes can be conceptually viewed as a graphene sheet rolled up into a nanoscale tube form to produce the single-walled carbon nanotubes (SWNTs). There may be additional graphene tubes around the core of a SWNT to form multiwalled carbon nanotubes (MWNTs). Depending on their diameter and helicity of the arrangement of carbon atoms in the nanotube walls, SWNTs can exhibit semiconducting or metallic behavior. Semiconducting SWNTs have been demonstrated to be very promising building blocks for constructing various electronic devices for a large variety of applications, ranging from chemical/biosensors to molecular electronics. For many of these applications, field effect transistors (FETs) made from SWNTs often play an important role.

One of the major hurdles for the widespread application of SWNTs in semiconductor electronics is the coexisting of metallic and semiconducting carbon nanotubes in the as-synthesized samples. Due to the presence of metallic tubes, FET characteristics (e.g., the on/off ratio and integration uniformity) become poor and uncontrollable. For instance, the on/off ratio is usually less than 10 for FETs based on nonseparated carbon nanotube network films, which is actually too small for any practical application. Therefore, it is critical to separate metallic nanotubes from semiconducting SWNTs for constructing nanotube-based semiconductor devices of high performance. A few separation approaches, including electrophoresis, tube-type-specific physicochemical modification, and selective elimination of metallic SWNTs by electrical breakdown, and laser irradiation have been devised. However, the problem associated with the coexisting metallic nanotubes has not yet been completely solved as the postsynthesis separation processes are often tedious and even involve possible contamination or degradation of nanotubes. It will be a significant advancement for the development of electronic devices from carbon nanotubes if we can preferentially synthesize semiconducting SWNTs for direct use in FETs. Indeed, chemical vapor deposition (CVD) methods with and without the plasma enhancement have been used for preferentially produce nonaligned SWNTs with a high percentage of semiconducting nanotubes (~90%) or SWNTs with a specific chirality distribution. Due to the random feature of nonaligned SWNTs, it is difficult to further increase the percentage of semiconducting nanotubes necessary for the high-throughput construction of electronic devices. Although the mechanism for preferentially growing the semiconducting SWNTs is still not well understood, these pioneering studies indicate the possibility for selective syntheses of semiconducting SWNTs by carefully controlling the growth parameters.

On the other hand, it is highly desirable to grow aligned/micropatterned SWNTs for electronic applications as their structure—property can be readily assessed while they can be effectively incorporated into devices. The vertically aligned structure will not only provide a well-defined large surface area for the device performance enhancement but also allow controlled functionalization of nanotubes along the tube length for incorporating multifunctionalities. Although the growth of aligned/micropatterned MWNTs has
been known for some years, the synthesis of aligned SWNT forests is a recent development. Along with others, we have used the combined PECVD and fast heating method to grow VA-SWNTs on a large scale, and to produce multicomponent micropatterns of VA-SWNTs interposted with VA-MWNTs. As far as we are aware, the selective growth of semiconducting VA-SWNTs has not been reported, which, if realized, should facilitate the development of various nanotube electronic devices. Here, we report our recent work on the preferential growth of semiconducting VA-SWNTs by using the combined PECVD and fast heating method with necessary modifications under a low pressure (30 mTorr) C₂H₂ flow as carbon source without additional carrier gas. As we shall see later, the resultant semiconducting VA-SWNTs can be directly used as the electronically active materials in FET devices even without any purification or separation.

In a typical experiment for the synthesis of semiconducting VA-SWNT arrays, a thin film of Fe (0.5 nm) was sputter-coated on a 10 nm thick Al layer precoated on the SiO₂ surface of a SiO₂/Si wafer. The Al coating was used to effectively prevent Fe catalyst nanoparticles from aggregation at the high temperature required for the growth of VA-SWNTs. To start the nanotube growth process, the catalyst-coated SiO₂/Si wafer was quickly moved from a cool zone (25 °C) into the center of a plasma-enhanced (80 W, 13.56 MHz) tube furnace heated at 750 °C under a pure gas flow of C₂H₂ (atomic absorption grade, 99.8%, Airgas Great Lakes, Inc.) at a low pressure of 30 mTorr. Three minutes later, the nanotube deposited SiO₂/Si wafer was taken out from the furnace for subsequent analyses without any purification.

Figures 1a and 1b reproduce scanning electron microscopic (Hitachi S-4800 high-resolution SEM) images, showing a homogeneous coverage of about 2–3 μm long well-aligned nanotubes over the whole substrate surface. A transmission electron microscopic (Hitachi H-7600 TEM unit) image for the constituent nanotubes taken from an ethanol dispersion of the VA-SWNT sample is given in Figure 1c, which reveals SWNT characteristics for each of the individual nanotubes (Figure 1c). Atomic force microscopic (AFM) analyses show apparent diameters in the range of 0.8–2 nm for the as-synthesized SWNTs (see Figure S1, Supporting Information). The corresponding Raman spectrum (inVia micro-Raman spectrometer, Renishaw) recorded with a 514 nm laser for the nanotube sample shown in Figure 1a clearly shows the radial breathing mode (RBM) for SWNTs (Figure 1d), from which the nanotube diameters were found to be in a good consistence with those measured by AFM.

The RBM of Raman spectra can be used to assign and quantitatively evaluate metallic and semiconducting nanotubes. To confirm the semiconducting properties of the as-synthesized VA-SWNTs, we systematically measured Raman spectra with laser beams of different excitation wavelengths. Commercial HiPco SWNTs with a known metallic-to-semiconducting ratio (ca. 40%-60%) were used as reference. Figure 2a shows the spectroscopic details over the RBM and G-band regions recorded with the 514 nm laser (2.41 eV) for the as-synthesized VA-SWNTs and HiPco SWNTs to give a qualitative indication of the relative population of semiconducting to metallic nanotubes in these samples. As expected, the HiPco SWNTs show...
typical RBM features for semiconducting nanotubes over ca. 150–210 cm\(^{-1}\) and metallic nanotubes in the region between about 210 and 280 cm\(^{-1}\), along with a strong and broad Breit–Wigner–Fano (BWF) line shape for the G-band associated with metallic nanotubes (curves with circular dots in Figure 2a).\(^8\),\(^{10a,c,f,29}\) In contrast, the corresponding Raman spectrum of the as-synthesized VA-SWNTs (curves with squares in Figure 2a) is dominated by spectroscopic features of semiconducting SWNTs in both the RBM and G-band regions, indicating the presence of a high percentage of semiconducting nanotubes. Further evidence for the high percentage of semiconducting nanotubes in the as-grown VA-SWNT sample comes from the corresponding Raman spectra measured with a 633, 488, and 785 nm wavelength laser, respectively. As seen in Figure 2b, the RBM modes of the as-grown VA-SWNTs recorded with 633 and 488 nm lasers reveal dominant features for semiconducting nanotubes (curves with squares in Figure 2b) while the corresponding spectra for HiPco SWNTs (curves with circles in Figure 2b) indicate the coexistence of semiconducting and metallic nanotubes. The above observed difference is consistent with Figure 2a. However, the RBM modes recorded with a 785 nm laser show extensive signals of semiconducting nanotubes for both the as-synthesized VA-SWNTs and HiPco sample as only semiconducting nanotubes are Raman active at this particular excitation wavelength.\(^{10c}\)

Although it is not easy to precisely measure the percentage of semiconducting nanotubes in the as-grown SWNT array, we can make a reasonable estimation from the integrated Raman peak intensities by using the commercial HiPco SWNTs as reference.\(^{10c,f,28c}\) In view of the known ratio of 40%-60% for metallic-to-semiconducting nanotubes in the commercial HiPco sample,\(^{10c,16a}\) we estimated about 96% of semiconducting nanotubes in the as-grown VA-SWNT array by comparing its integrated RBM intensity with that of HiPco SWNTs (Figure 2a). Similar calculations using the RBMs shown in Figure 2b recorded with 633 and 488 nm lasers also indicate more than 80% of semiconducting nanotubes in the as-grown VA-SWNT sample, though RBMs associated with the 514 nm laser have been demonstrated to provide the most accurate ratios of the metallic to semiconducting nanotubes in samples enriched with semiconducting nanotubes.\(^{10c}\) Photoluminescence excitation/emission contour plots of the as-synthesized SWNTs measured by simply dispersing them in D\(_2\)O with sodium dodecylbenzene sulfonate (SDBS) without any separation revealed the presence of mainly (7,5), (7,6), (8,4), (8,7), (9,5) and (10,2) semiconducting SWNTs (Figure 3).\(^{30}\)

It was demonstrated that the plasma enhancement played an important role in the selective growth of semiconducting VA-SWNTs, as is the case for growing their nonaligned counterparts.\(^{16}\) To ensure stable growth conditions for the preferential formation of semiconducting SWNTs, we have
used C$_2$H$_2$ only, without any carrier gas. This, together with the fast heating, could minimize any possible disturbance to the growing nanotubes, which otherwise may introduce defect(s) into the nanotube structure. Furthermore, we found that a well-controlled low gas pressure (30 mTorr of C$_2$H$_2$) is critical for the selective growth of semiconducting nanotubes as an increased gas flow rate often facilitates the growth of metallic nanotubes. Besides, the growth of semiconducting nanotubes was found to also depend strongly on the growth temperature and time. As seen in Figure 4, Raman peak intensities corresponding to metallic nanotubes with respect to those of semiconducting nanotubes increased with increasing temperature and growth time. Although these results indicate a narrow growth window for the preferential formation of semiconducting nanotubes, VA-SWNT arrays with more than 95% semiconducting nanotubes have been repeatedly synthesized by carefully controlling the growth parameters (e.g., gas flow rate, temperature, time).

The high yield of semiconducting SWNTs produced by the preferential synthesis described above promoted us to directly use the as-synthesized SWNTs for constructing nanotube FETs without any postsynthesis purification or separation, which is necessary for the use of other nanotube samples in FETs. Specifically, we dispersed 0.1 mg of the as-synthesized VA-SWNTs in 1 mL of DMF under ultrasonication (Branson, 2510R-MT, 100 W) for 10 min, followed by solution-casting the slightly dispersed nanotube bundles between the drain and source Au electrodes prefabricated on a SiO$_2$/Si wafer (400 nm thick SiO$_2$) to form a nanotube bundle network FET with the Si substrate to act as the gate electrode (Figure 5a). For comparison, we have also prepared a network FET based on HiPco SWNTs according to the same procedure. We deliberately chose bundled nanotubes instead of individual nanotubes for the FET investigation in order to demonstrate the high percentage

![Figure 5](image-url)

**Figure 5.** (a) A typical SEM image of the as-synthesized SWNT bundle network on two electrodes. Drain current vs drain voltage measured at gate voltages ranging from −30 to −5 V in 5 V steps for (b) the as-synthesized SWNT FET and (c) HiPco network device; and (d) drain current vs gate voltage for both the as-synthesized SWNT (−□−) and HiPco (−○−) network devices measured at a drain voltage of 0.02 V.
of semiconducting nanotubes in the as-grown VA-SWNT sample.

As can be seen in Figure 5b, FETs based on the as-synthesized SWNTs showed a typical field effect characteristic with the drain current increased with increasing negative gate voltage, indicating a p-type semiconductor for the SWNTs. In contrast, Figure 5c shows almost no field effect for the corresponding HiPco device. The variations of drain current with the gate voltage for a drain voltage of 0.02 V given in Figure 5d show an on/off ratio of more than 100 for the as-synthesized SWNT FET in air and a quasi-linear plot with a relatively small slope for the HiPco device. The on/off ratio of more than 100 for the as-synthesized SWNT network FET is comparable to that of planar FETs consisting of parallel SWNTs after electrical breakdown of metallic nanotubes. Since the source–drain channel width for our FETs (Figure 5a) is close to the nanotube length of as-synthesized VA-SWNT sample. The mobility ($\mu$) for the formation of a continuous network across the source and drain electrodes to cause any noticeable effect on the low percentage of metallic nanotubes randomly dispersed over long channel devices seems to be insignificant in the present study. Due also to the similar length scale between the nanotube length and channel width, the low percentage of metallic nanotubes dispersed in the semiconducting SWNT matrix could be insufficient for the formation of a continuous network across the source and drain electrodes to cause any noticeable effect on the FET performance. Once again, the above results clearly indicate the high yield of semiconducting nanotubes in the as-synthesized VA-SWNT sample. The mobility ($\mu$) of the as-synthesized SWNTs was estimated, according to the standard formula, to be ca. 11.4 cm$^2$/Vs in air, which is slightly higher than that of a planar FET after electrical breakdown of metallic nanotubes.

We have investigated more than 10 such FET devices based on the as-synthesized SWNTs and found the overall on/off ratio to be around 10$^2$ and mobility to be 4.2 to 13 cm$^2$/Vs (cf. Figure S2, Supporting Information). The relatively high mobility observed above indicates that the bundled semiconducting SWNT network could transport electrons efficiently. Besides, a FET device based on the as-synthesized VA-SWNT array with the source and drain electrodes directly deposited onto the top surface of the VA-SWNTs supported by a SiO$_2$/Si wafer (400 nm thick SiO$_2$ with the Si substrate acting as the gate electrode) has also been constructed (see the inset of Figure S3, Supporting Information). Our preliminary results indicate that the vertically constructed FET from the as-synthesized VA-SWNT array also shows a reasonably strong field effect, though it has yet been optimized (Figure S3).

In conclusion, we have demonstrated the first preferential synthesis of vertically aligned SWNTs with the percentage content of semiconducting nanotubes up to ca. 96% by using the combined plasma-enhanced CVD and fast heating method for pyrolysis of pure C$_2$H$_2$ under a low pressure (~30 mTorr). The high yield of the resultant semiconducting SWNTs was confirmed by systematic Raman spectroscopic measurements. Photoluminescence excitation/emission spectroscopic measurements revealed the presence of mainly (7,5), (7,6), (8,4), (8,7), (9,5) and (10,2) semiconducting SWNTs. The as-synthesized semiconducting SWNT arrays can be used directly for fabricating either vertically constructed or horizontally deposited nanotube network FET devices without any purification or separation. The nanotube network FETs showed a very good device performance, comparable to that of SWNT thin film FETs after electrical breakdown of metallic nanotubes. Therefore, these selectively grown semiconducting VA-SWNT arrays should be attractive for the development of various optoelectronic nanodevices with high performance.

Acknowledgment. We gratefully acknowledge the financial support from AFOSR (FA9550-06-1-0384) and NSF (NIRT-0609077). Thanks also to the NEST Laboratory at UD for the access of SEM and TEM facilities.

Supporting Information Available: Experimental details, an AFM image of the as-synthesized semiconducting SWNTs and the corresponding topographic height analysis, and plots of drain current vs gate voltage for the as-synthesized SWNT network and array. This material is available free of charge via the Internet at http://pubs.acs.org.

References

(7) See, for example: Japanese patent application P2005-45188.


NL800967N